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## Patent Abstracts of Japan

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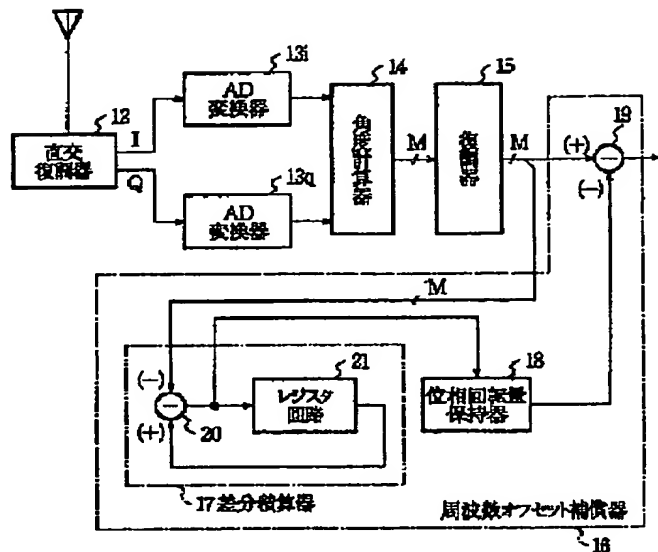
APPLICATION DATE : 24-12-93  
APPLICATION NUMBER : 05326772

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TITLE : DELAY DETECTING DEVICE FOR MULTIPHASE MODULATED SIGNAL



ABSTRACT : PURPOSE: To reduce demodulation distortion by finding a phase rotation quantity based upon a frequency offset during a preamble period, and holding the phase quantity even during a data signal period and subtracting it from a demodulation output.

CONSTITUTION: A demodulator 15 calculates the phase rotation quantity by calculating the difference between an angle calculated value of an angle calculator 14 which is one symbol precedent and a current angle calculated value, and inputs it to a frequency offset compensator 16. The difference integrator 17 of the compensator 16 integrates the difference in the angle calculated value of each symbol for a specific symbol section during preamble reception. The section-integrated phase rotation quantity is held by a phase rotation quantity holder 18 and the phase rotation quantity based upon the frequency offset is calculated from the mean value. The phase rotation quantity is continuously outputted as a fixed value during single data reception following a preamble signal and subtracted by a subtracter 19 from the output of the demodulator 15. Consequently, a transient offset is smoothed and stable demodulation is enabled.

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